

COMPLEX LAMINATED CHIP ELEMENT

Abstract

5 The present invention relates to a laminated chip element which can be manufactured to have desired electric properties by combining various elements in accordance with the desired objectives. More particularly, the present invention relates to a laminated chip element which has superior high frequency properties and can be manufactured to control capacitance and/or inductance of the laminated chip element to a desired value. There is
10 provided a laminated chip element, comprising at least one first sheet on which first and second conductive patterns are formed, the first and second conductive patterns being spaced apart from each other in a direction of both ends of the first sheet; and at least one second sheet on which a third conductive pattern is formed, the third conductive pattern being formed in a transverse direction of both the ends of the first sheet; wherein one ends of the
15 first and second conductive patterns are connected to the first and second external terminals, respectively, at least one end of the third conductive pattern is connected to a third external terminal, and the first and second sheets are laminated. There is also provided a laminated chip element, comprising: at least one first sheet on which a conductive pattern is formed, the first conductive pattern consisting of first and third portions, the first and second portions
20 being spaced apart from each other in a direction of both ends of the first sheet, the second portion connecting the first and second portions to each other to have a predetermined inductance; and at least one second sheet on which a conductive pattern is formed in a transverse direction of both the ends of the first sheet; wherein the first and second portions are connected to the first and second external terminals, respectively, at least one ends of the
25 second conductive pattern is connected to a third external terminal, and the first and second sheets are laminated.